

**REVERSE ENGINEERING**  
**MACHINE CODE: PART 1**

# Function Conventions

- Standard Entry Sequence (cdecl)
  - Save the old base pointer
  - Set the new stack base pointer
  - Allocate space for variables

```
__function:  
    push ebp        ; 55  
    mov  ebp, esp   ; 8BEC  
    sub  esp, x     ; Not always present
```

```
__function:  
    enter           ; C8  
    sub  esp, x     ; Not always present
```

# Function Conventions

- Standard Exit Sequence (cdecl)
  - Reload old stack pointer
  - Reload old stack base
  - Deallocate space for variables

```
...  
    mov esp, ebp    ; 8BE5  
    pop ebp        ; 5D  
    ret            ; C3 near, CB far
```

```
...  
    leave          ; C9  
    ret            ; C3 near, CB far
```



# Function Call Conventions

- cdecl
  - Used by GCC and GNU libraries
- stdcall
  - Used by Win32 API
  - Sometimes incorrectly called “pascal”
- fastcall
  - Many different implementations
  - Not standardized

# Function Call Conventions

- **cdecl**
  - Parameters pushed right to left
  - EAX, ECX, EDX not preserved
  - Return values are returned in EAX
    - Floating point returns in ST0
  - Caller performs clean-up
- **stdcall**
  - Same as cdecl, except callee cleans-up
    - `RET imm` is a sign of this
- **fastcall**
  - One or more parameters passed in registers
  - MS VC++, GCC
    - First arg → ECX, second arg → EDX, remainder right → left



# cdecl Function Call Convention

- Push Parameters on Stack
- Call the Function
- Save and Update EBP
- Save Registers that Will Be Overwritten
- Allocate Local Variables
- Execute Function
- Release Local Storage



# cdecl Function Call Convention

- Restore Saved Registers
- Restore EBP
- Return
- Clean Up Parameters



# stdcall Function Call Convention

- Push Parameters on Stack
- Call the Function
- Save and Update EBP
- Save Registers that Will Be Overwritten
- Allocate Local Variables
- Execute Function
- Release Local Storage





# stdcall Function Call Convention

- Restore Saved Registers
- Restore EBP
- Clean Up Parameters
- Return



# Function Call Conventions

- Others
  - pascal
    - Parameters pushed left to right
    - Windows 3.\*
  - syscall
    - Parameter size passed in AL
  - safecall
    - Encapsulated COM error handling
  - thiscall
    - Either caller or callee clean-up
  - ...



# Control Statements

- If-Else
- Switch
- For
- While

# If-Else Statement

```
#include <stdio.h>
#include <stdlib.h>

void do_something(int);

void main(int argc, char *argv[]) {
    do_something(1);
}

void do_something(int i) {
    if (i > 0)
        printf("Greater than zero\n");
    else
        printf("Not greater than zero\n");
}
```

# If-Else Statement

```
00401060 > 55 PUSH EBP
00401061 . 8BEC MOV EBP,ESP
00401063 . 83EC 40 SUB ESP,40
00401066 . 53 PUSH EBX
00401067 . 56 PUSH ESI
00401068 . 57 PUSH EDI
00401069 . 8D7D C0 LEA EDI,DWORD PTR SS:[EBP-40]
0040106C . B9 10000000 MOV ECX,10
00401071 . B8 CCCCCCCC MOV EAX,CCCCCCCC
00401076 . F3:AB REP STOS DWORD PTR ES:[EDI]
00401078 . 837D 08 00 CMP DWORD PTR SS:[EBP+8],0
0040107C . 7E 0F JLE SHORT if_state.0040108D
0040107E . 68 38004200 PUSH OFFSET if_state.??_C@_0BD@HFLA@Gre
00401083 . E8 78000000 CALL if_state.printf
00401088 . 83C4 04 ADD ESP,4
0040108B . EB 0D JMP SHORT if_state.0040109A
0040108D > 68 1C004200 PUSH OFFSET if_state.??_C@_0BH@HNHD@Not
00401092 . E8 69000000 CALL if_state.printf
00401097 . 83C4 04 ADD ESP,4
0040109A > 5F POP EDI
0040109B . 5E POP ESI
0040109C . 5B POP EBX
0040109D . 83C4 40 ADD ESP,40
004010A0 . 3BEC CMP EBP,ESP
004010A2 . E8 19000000 CALL if_state.__chkesp
004010A7 . 8BE5 MOV ESP,EBP
004010A9 . 5D POP EBP
004010AA . C3 RETN
```

[format = "Greater than zero"]  
printf

[format = "Not greater than zero"]  
printf

# Switch Statement

```
#include <stdio.h>
#include <stdlib.h>

void do_something(int);

void main(int argc, char *argv[]) {
    do_something(1);
}

void do_something(int i) {
    switch(i) {
        case 0:
        case 1:
            printf("Zero or one\n");
            break;
        case 2:
            printf("Two\n");
            break;
        default:
            break;
    }
}
```

# Switch Statement

00401060	> 55	PUSH EBP	
00401061	. 8BEC	MOV EBP,ESP	
00401063	. 83EC 44	SUB ESP,44	
00401066	. 53	PUSH EBX	
00401067	. 56	PUSH ESI	
00401068	. 57	PUSH EDI	
00401069	. 8D7D BC	LEA EDI,DWORD PTR SS:[EBP-44]	
0040106C	. B9 11000000	MOV ECX,11	
00401071	. B8 CCCCCCCC	MOV EAX,CCCCCCCC	
00401076	. F3:AB	REP STOS DWORD PTR ES:[EDI]	
00401078	. 8B45 08	MOV EAX,DWORD PTR SS:[EBP+8]	
0040107B	. 8945 FC	MOV DWORD PTR SS:[EBP-4],EAX	
0040107E	. 837D FC 00	CMP DWORD PTR SS:[EBP-4],0	
00401082	.v7C 2A	JL SHORT switch_s.004010AE	
00401084	. 837D FC 01	CMP DWORD PTR SS:[EBP-4],1	
00401088	.v7E 08	JLE SHORT switch_s.00401092	
0040108A	. 837D FC 02	CMP DWORD PTR SS:[EBP-4],2	
0040108E	.v74 11	JE SHORT switch_s.004010A1	
00401090	.vEB 1C	JMP SHORT switch_s.004010AE	
00401092	> 68 24004200	PUSH OFFSET switch_s.??_C@_0N@OGF@Zero?	[format = "Zero or one"]
00401097	. E8 84000000	CALL switch_s.printf	printf
0040109C	. 83C4 04	ADD ESP,4	
0040109F	.vEB 0D	JMP SHORT switch_s.004010AE	
004010A1	> 68 1C004200	PUSH OFFSET switch_s.??_C@_04GGPI@Two?	[format = "Two"]
004010A6	. E8 75000000	CALL switch_s.printf	printf
004010AB	. 83C4 04	ADD ESP,4	
004010AE	> 5F	POP EDI	
004010AF	. 5E	POP ESI	
004010B0	. 5B	POP EBX	
004010B1	. 83C4 44	ADD ESP,44	
004010B4	. 3BEC	CMP EBP,ESP	
004010B6	. E8 25000000	CALL switch_s.__chkesp	
004010BB	. 8BE5	MOV ESP,EBP	
004010BD	. 5D	POP EBP	
004010BE	. C3	RETN	

# For Statement

```
#include <stdio.h>
#include <stdlib.h>

void do_something(int);

void main(int argc, char *argv[]) {
    do_something(10);
}

void do_something(int i) {
    for(; i > 0; i--)
        printf("%d\n", i);
}
```



# For Statement

00401060	> 55	PUSH EBP	
00401061	. 8BEC	MOV EBP,ESP	
00401063	. 83EC 40	SUB ESP,40	
00401066	. 53	PUSH EBX	
00401067	. 56	PUSH ESI	
00401068	. 57	PUSH EDI	
00401069	. 807D C0	LEA EDI,DWORD PTR SS:[EBP-40]	
0040106C	. B9 10000000	MOV ECX,10	
00401071	. B8 CCCCCCCC	MOV EAX,CCCCCCCC	
00401076	. F3:AB	REP STOS DWORD PTR ES:[EDI]	
00401078	.v EB 09	JMP SHORT for_stat.00401083	
0040107A	> 8B45 08	MOV EAX,DWORD PTR SS:[EBP+8]	
0040107D	. 83E8 01	SUB EAX,1	
00401080	. 8945 08	MOV DWORD PTR SS:[EBP+8],EAX	
00401083	> 837D 08 00	CMPL DWORD PTR SS:[EBP+8],0	
00401087	.v 7E 13	JLE SHORT for_stat.0040109C	
00401089	. 8B4D 08	MOV ECX,DWORD PTR SS:[EBP+8]	
0040108C	. 51	PUSH ECX	
0040108D	. 68 1C004200	PUSH OFFSET for_stat.??_C@_03HMFC@?%CF.	<%d>
00401092	. E8 69000000	CALL for_stat.printf	format = "%d"
00401097	. 83C4 08	ADD ESP,8	printf
0040109A	.^ EB DE	JMP SHORT for_stat.0040107A	
0040109C	> 5F	POP EDI	
0040109D	. 5E	POP ESI	
0040109E	. 5B	POP EBX	
0040109F	. 83C4 40	ADD ESP,40	
004010A2	. 3BEC	CMP EBP,ESP	
004010A4	. E8 17000000	CALL for_stat.__chkesp	
004010A9	. 8BE5	MOV ESP,EBP	
004010AB	. 5D	POP EBP	
004010AC	. C3	RETN	

# While Statement

```
#include <stdio.h>
#include <stdlib.h>

void do_something(int);

void main(int argc, char *argv[]) {
    do_something(10);
}

void do_something(int i) {
    while(i > 0) {
        printf("%d\n", i);
        i--;
    }
}
```

# While Statement

00401060	> 55	PUSH EBP	
00401061	. 8BEC	MOV EBP,ESP	
00401063	. 83EC 40	SUB ESP,40	
00401066	. 53	PUSH EBX	
00401067	. 56	PUSH ESI	
00401068	. 57	PUSH EDI	
00401069	. 8D7D C0	LEA EDI,DWORD PTR SS:[EBP-40]	
0040106C	. B9 10000000	MOV ECX,10	
00401071	. B8 CCCCCCCC	MOV EAX,CCCCCCCC	
00401076	. F3:AB	REP STOS DWORD PTR ES:[EDI]	
00401078	> 837D 08 00	CMP DWORD PTR SS:[EBP+8],0	
0040107C	√ 7E 1C	JLE SHORT while_st.0040109A	
0040107E	. 8B45 08	MOV EAX,DWORD PTR SS:[EBP+8]	
00401081	. 50	PUSH EAX	
00401082	. 68 1C004200	PUSH OFFSET while_st.??_C@_03HMFC@?.\$CF	<%d>
00401087	. E8 74000000	CALL while_st.printf	format = "%d"
0040108C	. 83C4 08	ADD ESP,8	printf
0040108F	. 8B4D 08	MOV ECX,DWORD PTR SS:[EBP+8]	
00401092	. 83E9 01	SUB ECX,1	
00401095	. 894D 08	MOV DWORD PTR SS:[EBP+8],ECX	
00401098	^ EB DE	JMP SHORT while_st.00401078	
0040109A	> 5F	POP EDI	
0040109B	. 5E	POP ESI	
0040109C	. 5B	POP EBX	
0040109D	. 83C4 40	ADD ESP,40	
004010A0	. 3BEC	CMP EBP,ESP	
004010A2	. E8 19000000	CALL while_st.__chkesp	
004010A7	. 8BE5	MOV ESP,EBP	
004010A9	. 5D	POP EBP	
004010AA	. C3	RETN	



# Determining Signed-ness


- Signed and Unsigned Variables
  - Operations on signed/unsigned variables use different instructions
  - IMUL/MUL
  - IDIV/DIV
  - Jcc

# Determining Signed-ness

Instruction Mnemonic	Condition (Flag States)	Description
<b>Unsigned Conditional Jumps</b>		
JNBE	$(CF \text{ or } ZF) = 0$	Above/not below or equal
JAE/JNB	$CF = 0$	Above or equal/not below
JB/JNAE	$CF = 1$	Below/not above or equal
JBE/JNA	$(CF \text{ or } ZF) = 1$	Below or equal/not above
JC	$CF = 1$	Carry
JE/JZ	$ZF = 1$	Equal/zero
JNC	$CF = 0$	Not carry
JNE/JNZ	$ZF = 0$	Not equal/not zero
JNP/JPO	$PF = 0$	Not parity/parity odd
JP/JPE	$PF = 1$	Parity/parity even
JCXZ	$CX = 0$	Register CX is zero
JECXZ	$ECX = 0$	Register ECX is zero
<b>Signed Conditional Jumps</b>		
JG/JNLE	$((SF \text{ xor } OF) \text{ or } ZF) = 0$	Greater/not less or equal
JGE/JNL	$(SF \text{ xor } OF) = 0$	Greater or equal/not less
JL/JNGE	$(SF \text{ xor } OF) = 1$	Less/not greater or equal
JLE/JNG	$((SF \text{ xor } OF) \text{ or } ZF) = 1$	Less or equal/not greater
JNO	$OF = 0$	Not overflow
JNS	$SF = 0$	Not sign (non-negative)
JO	$OF = 1$	Overflow
JS	$SF = 1$	Sign (negative)



# Tools of the Trade

- Disassembler
    - Machine code to instructions
  - Decompiler
    - Instructions to code (often to C code)
  - Debugger
    - Real-time, step-thru-code debugging
- 

# Disassemblers

- Disassemblers
  - Converts machine code to instructions

010040F1	. FF75 08	PUSH DWORD PTR SS:[EBP+8]
010040F4	. FFD0	CALL EAX
010040F6	> 5E	POP ESI
010040F7	. C9	LEAVE
010040F8	. C2 1000	RETN 10
010040FB	⋄ 55	PUSH EBP
010040FC	. 8BEC	MOV EBP,ESP
010040FE	. 51	PUSH ECX
010040FF	. 51	PUSH ECX
01004100	. 8D45 FC	LEA EAX,DWORD PTR SS:[EBP-4]
01004103	. 56	PUSH ESI
01004104	. 33F6	XOR ESI,ESI
01004106	. 50	PUSH EAX
01004107	. 68 19000200	PUSH 20019
0100410C	. 56	PUSH ESI



# Decompilers

- Decompilers
  - Attempt to convert instructions or byte codes to higher-level languages
  - Good decompilers are implemented via p-code analysis
    - Allows decompiler code to be applied to various architectures as long as a p-code translation exists





# Debuggers

- Debuggers
  - Modes
    - User-mode
    - Kernel-mode
  - Common features
    - Create/attach to a process
    - Set/clear breakpoint
    - Step into/over
    - Trace into/over



# Debuggers

- Breakpoints
  - Software breakpoints
    - INT 3h (\xCC)
  - Memory breakpoints
  - Hardware breakpoints
    - Intel Dr0-Dr7 registers
- Traces
  - Records instructions and execution contexts
- Stepping
  - Step into/over



# GNU Debugger (gdb)

- Disassembler, Debugger
  - Command-line
    - Insight is a GUI wrapper for gdb
  - Not just for Linux
    - Native x86 Windows support
    - Special versions for various architectures

# GNU Debugger (gdb)

## Breakpoint Tutorial

```
jojo@grey:~> gdb hello_world
GNU gdb 6.6.50.20070726-cvs
Copyright (C) 2007 Free Software Foundation, Inc.
GDB is free software, covered by the GNU General Public License, and you are
welcome to change it and/or distribute copies of it under certain conditions.
Type "show copying" to see the conditions.
There is absolutely no warranty for GDB.  Type "show warranty" for details.
This GDB was configured as "i586-suse-linux"...
Using host libthread_db library "/lib/libthread_db.so.1".
(gdb) disas main
Dump of assembler code for function main:
0x08048238 <main+0>:   lea    0x4(%esp),%ecx
0x0804823c <main+4>:   and    $0xffffffff0,%esp
0x0804823f <main+7>:   pushl  -0x4(%ecx)
0x08048242 <main+10>:  push  %ebp
0x08048243 <main+11>:  mov    %esp,%ebp
0x08048245 <main+13>:  push  %ecx
0x08048246 <main+14>:  sub    $0x4,%esp
0x08048249 <main+17>:  movl  $0x809fd48,(%esp)
0x08048250 <main+24>:  call  0x8048c50 <puts>
0x08048255 <main+29>:  mov    $0x0,%eax
0x0804825a <main+34>:  add    $0x4,%esp
0x0804825d <main+37>:  pop    %ecx
0x0804825e <main+38>:  pop    %ebp
0x0804825f <main+39>:  lea   -0x4(%ecx),%esp
0x08048262 <main+42>:  ret
End of assembler dump.
(gdb)
```

# GNU Debugger (gdb) Breakpoint Tutorial

```
(gdb) break *0x08048249
Breakpoint 1 at 0x8048249
(gdb) run
Starting program: /home/jojo/hello_world

Breakpoint 1, 0x08048249 in main ()
(gdb) x/s0x0809fd48
0x809fd48:      "Hello World"
(gdb) c
Continuing.
Hello World

Program exited normally.
(gdb) q
jojo@grey:~> █
```



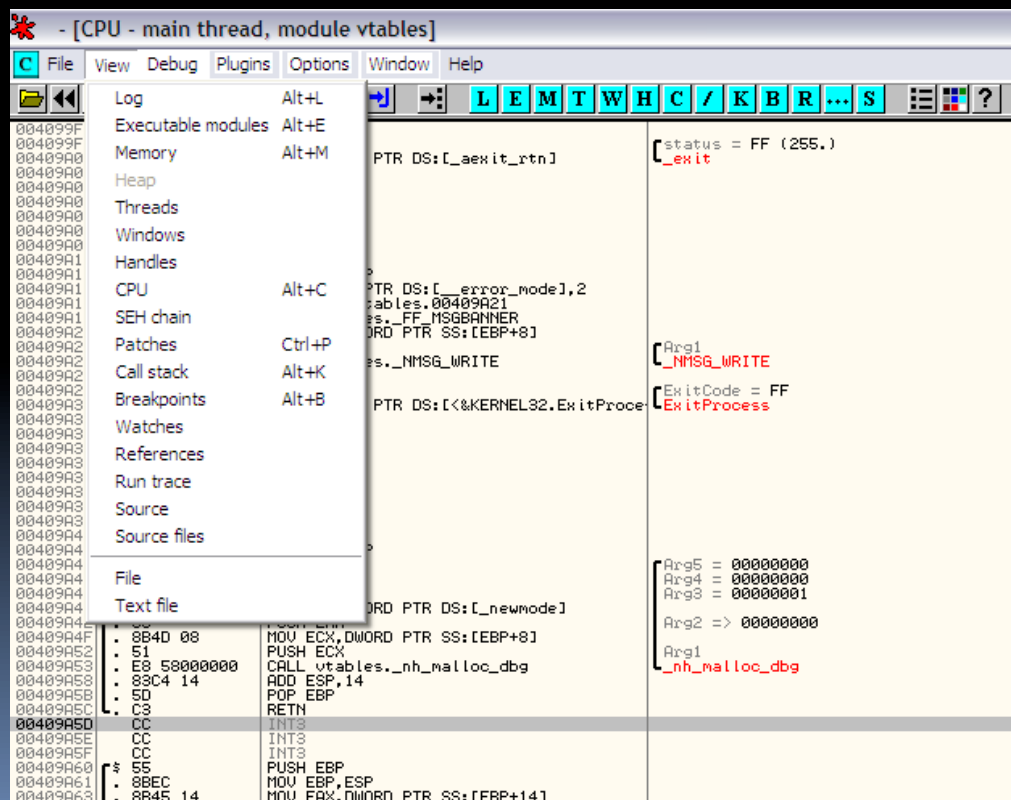
# OlllyDbg

- Disassembler
- Debugger
  - Open
    - Creates a process with debug privileges
  - Attach
    - Attach to a process already running
  - Detach (version 2.\*)
    - Detaches the debugger and allows the process to continue
  - Terminate
    - Kills the debuggee

# OlllyDbg

## Views

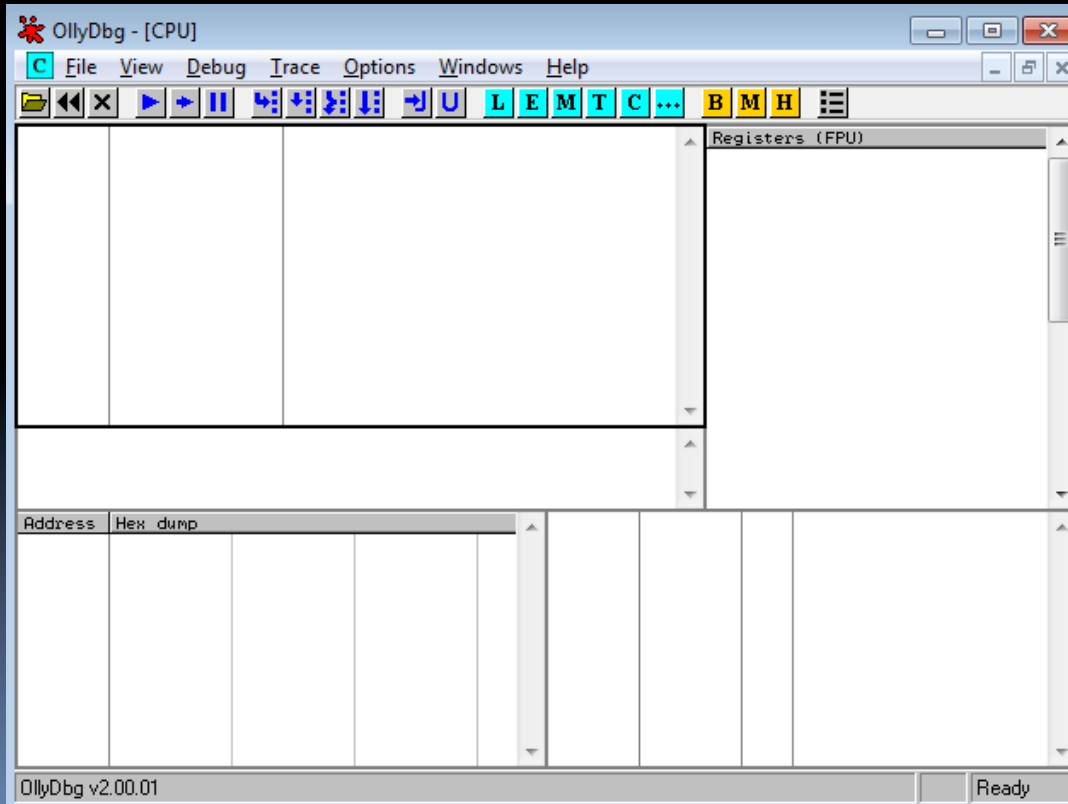
L E M T W H C / K B R ... S



Log	Alt+L
Executable modules	Alt+E
Memory	Alt+M
Heap	
Threads	
Windows	
Handles	
CPU	Alt+C
SEH chain	
Patches	Ctrl+P
Call stack	Alt+K
Breakpoints	Alt+B
Watches	
References	
Run trace	
Source	
Source files	

# OlllyDbg 2.0

- Views



Log	Alt+L
Executable modules	Alt+E
Memory map	Alt+M
Threads	Alt+T
CPU	Alt+C
Watches	Alt+V
Search results	
Run trace	
INT3 breakpoints	Alt+B
Memory breakpoints	Alt+Y
Hardware breakpoints	Alt+H
File...	



# OlllyDbg

- Code Analysis
  - Right-click → Analysis → Analyse code (Ctrl + A)
  - Static code analysis
    - Argument labeling
    - Function address name resolution
    - Control logic labeling

```
PUSH 0
PUSH 0
PUSH 1
MOV EAX,DWORD PTR DS:[_newmode]
PUSH EAX
MOV ECX,DWORD PTR SS:[EBP+8]
PUSH ECX
CALL vtables._nh_malloc_dbg
```

```
Arg5 = 00000000
Arg4 = 00000000
Arg3 = 00000001
Arg2 => 00000000
Arg1
_nh_malloc_dbg
```



# OlllyDbg

- Just-in-time Debugger
  - Options → Just-in-time debugging
  - Runs Olly when a fatal error occurs
- Plugins
  - Great feature
  - Well used by the reverse engineering community

# OlllyDbg Debugging Tutorial

- Breakpoints
  - Set a breakpoint (F2)

The screenshot displays the OlllyDbg interface with the following components:

- Assembly View:** Shows assembly instructions for a function. The instruction at address 00401078 is highlighted: `CMP DWORD PTR SS:[EBP+8],0`. Comments indicate that the next instruction is a `JLE` (Jump if Less or Equal) and the following is a `JMP` (Jump).
- Registers (FPU):** Lists the state of various registers. Notable values include `EAX: 00000000`, `ECX: 0012FFB0`, `EDX: 7C90E514`, `EIP: 00401180`, and `EFL: 0000246`.
- Hex Dump:** Located at the bottom, it shows memory data starting at address 00422000. The current address is 0012FFC4, which contains the value 7C817077. The dump shows a sequence of bytes and their corresponding hex values.

# OlllyDbg Debugging Tutorial

- Breakpoints
  - Resume execution (F9)

The screenshot displays the OlllyDbg interface with the following components:

- Assembly View:** Shows assembly instructions from address 00401060 to 004010A9. The instruction at 00401078 is highlighted in red. Comments for instructions at 00401088 and 00401092 are visible: "format = 'Greater than printf'" and "format = 'Not greater than printf'".
- Registers (FPU):** Lists registers EAX through EIP with their current values. EIP is 00401060, pointing to if\_state.do\_something.
- Registers (GPR):** Lists registers ST0 through ST7, all showing empty or zero values.
- Registers (FPU):** Lists floating-point registers FST through FCW with their values.
- Hex Dump:** Located at the bottom, showing memory addresses from 00422000 to 00422048 with their corresponding hex values.
- Status Bar:** Shows "EBP=0012FF80", "Jump from 0040100A", and "if\_statement.c:11. void do\_something(int i) {".

# OlllyDbg Debugging Tutorial

- Breakpoints
  - Resume execution (F9)

The screenshot displays the OlllyDbg interface with the following components:

- Assembly View:** Shows assembly instructions from address 00401060 to 004010AA. The instruction at 00401078 is highlighted: `CMP DWORD PTR SS:[EBP+8],0`. Comments indicate the next instruction is `JLE SHORT if_state.0040108D` if the value is "Greater than" and `JMP SHORT if_state.0040109A` if "Not greater than".
- Registers (FPU):** Lists CPU registers with their values: EAX (CCCCCCC), ECX (00000000), EDX (00430050), EBX (7FFDB000), ESP (0012FEDC), EBP (0012FF28), ESI (FFFFFFFF), EDI (0012FF28), and EIP (00401078).
- Stack:** Shows the current stack pointer at `SS:[0012FF30]=00000001`.
- Hex Dump:** A table showing memory addresses and their corresponding hex values. The current address is 0012FEDC.

Address	Hex dump
00422000	00 00 00 00 00 00 00 00
00422008	00 00 00 00 00 00 00 00
00422010	00 00 00 00 00 00 00 00
00422018	00 00 00 00 00 00 00 00
00422020	00 00 00 00 00 00 00 00
00422028	00 00 00 00 00 00 00 00
00422030	00 00 00 00 00 00 00 00
00422038	00 00 00 00 00 00 00 00
00422040	00 00 00 00 00 00 00 00
00422048	00 00 00 00 00 00 00 00

# OlllyDbg Debugging Tutorial

- Breakpoints
- Stack view

The screenshot displays the OlllyDbg interface with the following components:

- Assembly View:** Shows assembly instructions from address 00401060 to 004010AA. The instruction at 00401078 is highlighted: `CMP DWORD PTR SS:[EBP+8],0`. Comments on the right indicate `[format = "Greater than printf"]` and `[format = "Not greater than printf"]`.
- Registers (FPU):** Lists CPU registers with values: EAX: CCCCCCCC, ECX: 00000000, EDX: 00430050, EBX: 7FFDB000, ESP: 0012FEDC, EBP: 0012FF28, ESI: FFFFFFFF, EDI: 0012FF28, EIP: 00401078. Floating-point registers (FPU) are listed as empty or NULL.
- Stack View:** Shows the stack at address 00422000. The current instruction pointer (EIP) is 0012FF30, which is highlighted in red and labeled `RETURN to if_state.main+1F from if_state.0040100A`. The stack contains several `CCCCCCCC` values and the address `7C910228`.

# OlllyDbg Debugging Tutorial

- Stepping
  - Step into (F7)

The screenshot displays the OlllyDbg interface with the following components:

- Assembly View:** Shows assembly instructions from address 00401060 to 004010AA. The instruction at 0040107C is highlighted, showing a jump that is not taken. Comments on the right indicate the format of the printf calls: "[format = \"Greater than printf\"]" and "[format = \"Not greater than printf\"]".
- Registers (FPU):** Lists the state of various registers including EAX, ECX, EDI, ESP, and EIP. The EIP register is set to 0040107C, pointing to the current instruction.
- Memory Dump:** Located at the bottom, it shows a hex dump of memory starting at address 00422000. The current instruction address 0012FE0C is highlighted.
- Status Bar:** At the bottom, it indicates "Jump is NOT taken" and "00401080=if\_state.00401080".

# OlllyDbg Debugging Tutorial

- Stepping
  - Step into (F7)

The screenshot displays the OlllyDbg interface during a debugging session. The main window shows assembly code with the instruction at address 0040107E selected. The registers window on the right shows the current state of the CPU registers. The memory dump at the bottom shows the contents of memory addresses starting from 00422000.

```
00401060 > 55 PUSH EBP
00401061 . 8BEC MOV EBP,ESP
00401063 . 83EC 40 SUB ESP,40
00401066 . 53 PUSH EBX
00401067 . 56 PUSH ESI
00401068 . 57 PUSH EDI
00401069 . 8D7D C0 LEA EDI,DWORD PTR SS:[EBP-40]
0040106C . B9 10000000 MOV ECX,10
00401071 . B8 CCCCCCCC MOV EAX,CCCCCCCC
00401076 . F3:AB REP STOS DWORD PTR ES:[EDI]
00401078 . 837D 08 00 CMP DWORD PTR SS:[EBP+8],0
0040107C . 7E 0F JLE SHORT if_state.0040108D
0040107E . 68 30004200 PUSH OFFSET if_state.??_C@_0BD...
00401083 . E8 78000000 CALL if_state.printf
00401088 . 83C4 04 ADD ESP,4
0040108B . <EB 0D JMP SHORT if_state.0040109A
0040108D > 68 1C004200 PUSH OFFSET if_state.??_C@_0BD...
00401092 . E8 69000000 CALL if_state.printf
00401097 . 83C4 04 ADD ESP,4
0040109A > 5F POP EDI
0040109E . 5E POP ESI
0040109C . 5B POP EBX
0040109D . 83C4 40 ADD ESP,40
004010A0 . 3BEC CMP EBP,ESP
004010A2 . E8 19000000 CALL if_state.__chkesp
004010A7 . 8BE5 MOV ESP,EBP
004010A9 . 5D POP EBP
004010AA . C3 RETN
```

Registers (FPU)

EAX	CCCCCCCC
ECX	00000000
EDX	00430050
EBX	7FFDB000
ESP	0012FEDC
EBP	0012FF28
ESI	FFFFFFFF
EDI	0012FF28
EIP	0040107E if_state.0040107E
C 0	ES 0023 32bit 0(FFFFFFFF)
P 0	CS 001B 32bit 0(FFFFFFFF)
A 0	SS 0023 32bit 0(FFFFFFFF)
Z 0	DS 0023 32bit 0(FFFFFFFF)
S 0	FS 003B 32bit 7FFDF000(FFF)
T 0	GS 0000 NULL
D 0	
O 0	LastErr ERROR_SUCCESS (00000000)
EFL	00000202 (NO,NB,NE,A,NS,PO,GE,...
ST0	empty -UNORM BDEC 01050104 000
ST1	empty 0.0
ST2	empty 0.0
ST3	empty 0.0
ST4	empty 0.0
ST5	empty 0.0
ST6	empty 0.0
ST7	empty 0.0
FST	0000 Cond 0 0 0 0 Err 0 0 0
FCW	027F Prec NEAR,53 Mask 1

00420038=OFFSET if\_state.??\_C@\_0BD@HFLA@Greater?5than?5zero?6?%AA@ (ASCII "Greater t

if\_statement.c:13. printf("Greater than zero\n");

Address	Hex dump	0012FEDC	0012FF00
00422000	00 00 00 00 00 00 00 00 01	0012FEE0	FFFFFFFF
00422008	00 00 00 00 00 00 00 00 01	0012FEE4	7FFDB000
00422010	00 00 00 00 00 00 00 00 01	0012FEE8	CCCCCCCC
00422018	00 00 00 00 00 00 00 00 01	0012FEEC	CCCCCCCC
00422020	00 00 00 00 00 00 00 00 01	0012FEF0	CCCCCCCC
00422028	00 00 00 00 00 00 00 00 01	0012FEF4	CCCCCCCC
00422030	00 00 00 00 00 00 00 00 01	0012FEF8	CCCCCCCC
00422038	00 00 00 00 00 00 00 00 01	0012FEFC	CCCCCCCC
00422040	00 00 00 00 00 00 00 00 01	0012FF00	CCCCCCCC
00422048	00 00 00 00 00 00 00 00 01	0012FF04	CCCCCCCC
00422048	00 00 00 00 00 00 00 00 01	0012FF08	CCCCCCCC



# OlllyDbg Debugging Tutorial

- Stepping
  - Step into (F7)

The screenshot displays the OlllyDbg interface during a debugging session. The main window shows assembly code with the instruction at address 0040107E highlighted in red, indicating the current instruction pointer (EIP). The instruction is `PUSH OFFSET if_state.??_C0_0BD`. The registers window on the right shows the current state of the CPU registers, with `EIP 00401083` and `if_state.00401083` visible. The memory dump at the bottom shows the hex dump of the current instruction and surrounding memory.

```
00401060 > 55      PUSH EBP
00401061 . 8BEC    MOV EBP,ESP
00401063 . 83EC 40  SUB ESP,40
00401066 . 53      PUSH EBX
00401067 . 56      PUSH ESI
00401068 . 57      PUSH EDI
00401069 . 8D7D C0 LEA EDI,DWORD PTR SS:[EBP-40]
0040106C . B9 10000000 MOV ECX,10
00401071 . B8 CCCCCCCC MOV EAX,CCCCCCCC
00401076 . F3:AB   REP STOS DWORD PTR ES:[EDI]
00401078 . 837D 08 00 CMP DWORD PTR SS:[EBP+8],0
0040107C . 7E 0F   JLE SHORT if_state.00401080
0040107E . 68 38004200 PUSH OFFSET if_state.??_C0_0BD
00401083 . E8 78000000 CALL if_state.printf [format = "Greater than
00401088 . 83C4 04 ADD ESP,4
0040108B . EB 0D   JMP SHORT if_state.0040109A
0040108D > 68 1C004200 PUSH OFFSET if_state.??_C0_0BD [format = "Not greater t
00401092 . E8 69000000 CALL if_state.printf [format = "Not greater t
00401097 . 83C4 04 ADD ESP,4
0040109A > 5F      POP EDI
0040109B . 5E      POP ESI
0040109C . 5B      POP EBX
0040109D . 83C4 40 ADD ESP,40
004010A0 . 3BEC    CMP EBP,ESP
004010A2 . E8 19000000 CALL if_state.__chkesp
004010A7 . 8BES    MOV ESP,EBP
004010A9 . 5D      POP EBP
004010AA . C3      RETN
00401100=if_state.printf
if_statement.c:13. printf("Greater than zero\n");
Address Hex dump 0012FE08 00420038 [format = "Greater than zero"]
00422000 00 00 00 00 00 00 00 01 0012FE0C 0012FF80
00422008 00 00 00 00 00 00 00 01 0012FEE0 FFFFFFFF
00422010 00 00 00 00 00 00 00 01 0012FEE4 7FFDF000
00422018 00 00 00 00 00 00 00 01 0012FEE8 CCCCCCCC
00422020 00 00 00 00 00 00 00 01 0012FEEC CCCCCCCC
00422028 00 00 00 00 00 00 00 01 0012FEF0 CCCCCCCC
00422030 00 00 00 00 00 00 00 01 0012FEF4 CCCCCCCC
00422038 00 00 00 00 00 00 00 01 0012FEF8 CCCCCCCC
00422040 00 00 00 00 00 00 00 01 0012FEFC CCCCCCCC
00422048 00 00 00 00 00 00 00 01 0012FF00 CCCCCCCC
```

# OlllyDbg Debugging Tutorial

- Stepping
  - Let's say we step into (F7)

The screenshot displays the OlllyDbg interface with the following components:

- Assembly View:** Shows assembly instructions from address 00401100 to 00401149. The instruction at 00401100 is `PUSH EBP`. The instruction at 00401115 is `JNZ SHORT if_state.00401133`. The instruction at 00401137 is `JNZ SHORT if_state.0040110F`. The instruction at 00401138 is `CALL if_state._CrtDbgReport`. The instruction at 00401139 is `PUSH if_state.00422A88`. The instruction at 0040113A is `CALL if_state._stbuf`. The instruction at 0040113B is `ADD ESP,4`. The instruction at 0040113C is `MOV DWORD PTR SS:[EBP-4],EAX`. The instruction at 0040113D is `MOV EDX,DWORD PTR SS:[EBP-C]`.
- Registers (FPU):** Shows the state of registers. EAX is CCCCCCCC, ECX is 00000000, EDX is 00430D50, EBX is 7FFDB000, ESP is 0012FED4, EBP is 0012FF28, ESI is FFFFFFFF, EDI is 0012FF28. EIP is 00401100. The stack frame is `if_state.printf`. Other registers (C, P, A, Z, S, T, D, O) are empty or NULL. EFL is 00002020. ST0-ST7 are empty.
- Call Stack:** Shows the current call stack. The top frame is `CALL to printf from if_state.00401088` with format `"Greater than zero"`. The address is 0012FED4.
- Hex Dump:** Shows the memory dump starting at address 00422000. The dump contains several zero bytes followed by FFFFFFFF, 7FFDB000, and CCCCCCCC.

# OlllyDbg Debugging Tutorial

- Stepping
  - Let's say we step over (F8)

The screenshot displays the OlllyDbg interface with the following components:

- Assembly View:** Shows assembly instructions from address 00401060 to 004010A9. The instruction at 00401078 is highlighted in red, indicating the current instruction pointer (EIP). Comments for the instructions at 00401078 and 00401092 are visible: "format = 'Greater than printf'" and "format = 'Not greater t printf'".
- Registers (FPU):** Lists the state of various registers. EAX is 00000012, ECX is 00422A88, EDX is 00422A88, and EIP is 00401088. Other registers like ESP, EBP, ESI, and EDI are also shown.
- Memory Dump:** Located at the bottom, it shows the memory dump for address 0012FE08, displaying hex values and their ASCII representation: "Greater than zero".

# OlllyDbg Assembly Patching Tutorial

The screenshot shows the OlllyDbg interface with the following components:

- Assembly View:** Displays assembly instructions for the 'keygenme' module. The instruction at address 0040BA11 is highlighted: `JE SHORT keygenme.0040BA22`. Comments on the right indicate the expected format for license keys: `format = "License key must be 16 cha..."` and `format = "Valid license key!?!?"`.
- Registers (F):** Shows the current state of CPU registers. EAX is 00000000, ECX is 0012FFB0, and EIP is 00401260.
- Hex Dump:** Located at the bottom, it shows a memory dump starting at address 00422000. The dump contains several null bytes followed by a sequence of hex values: 0012FF6C, 85C4A2C8, 0012FF70, AAF30C6C, 0012FF74, 00000000, 0012FF78, E2C4D008, 0012FF7C, 00000000, 0012FF80, 85DE4FE0, 0012FF84, 00001FE0, 0012FF88, 85C4A200, 0012FF8C, E2C4D408, 0012FF90, 00000001, 0012FF94, 00000000, 0012FF98, 00000408.
- Status Bar:** Indicates the program is 'Paused' and provides statistics: 'Analysing keygenme: 161 heuristical procedures, 201 calls to known, 244 calls to guessed functions'.

# OlllyDbg

## Assembly Patching Tutorial

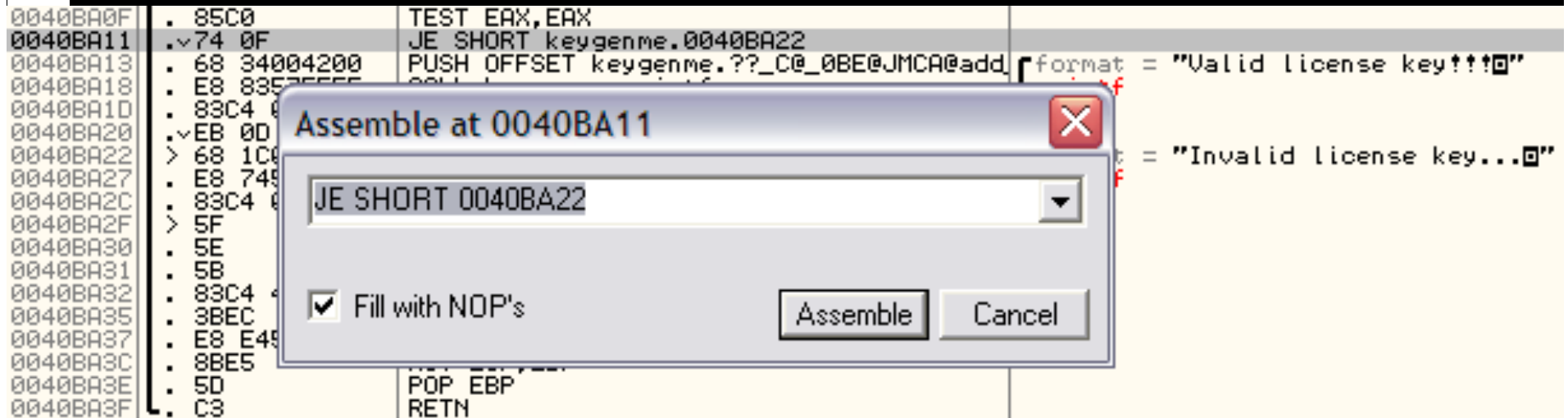
- Assembly Patching
  - Change the jump from “invalid” code to “valid” code

0040BA06	. 52	PUSH EDX	
0040BA07	. E8 F955FFFF	CALL keygenme.00401005	
0040BA0C	. 83C4 04	ADD ESP,4	
0040BA0F	. 85C0	TEST EAX,EAX	
0040BA11	√ 74 0F	JE SHORT keygenme.0040BA22	
0040BA13	. 68 34004200	PUSH OFFSET keygenme.??_C@_0BE@JMCA@add	[format = "Valid license key!!!"
0040BA18	. E8 8357FFFF	CALL keygenme.printf	printf
0040BA1D	. 83C4 04	ADD ESP,4	
0040BA20	√ EB 0D	JMP SHORT keygenme.0040BA2F	
0040BA22	> 68 1C004200	PUSH OFFSET keygenme.??_C@_0BF@CGOC@xor	[format = "Invalid license key..."
0040BA27	. E8 7457FFFF	CALL keygenme.printf	printf
0040BA2C	. 83C4 04	ADD ESP,4	
0040BA2F	> 5F	POP EDI	
0040BA30	. 5E	POP ESI	
0040BA31	. 5B	POP EBX	
0040BA32	. 83C4 40	ADD ESP,40	
0040BA35	. 3BEC	CMP EBP,ESP	
0040BA37	. E8 E457FFFF	CALL keygenme.__chkesp	
0040BA3C	. 8BE5	MOV ESP,EBP	
0040BA3E	. 5D	POP EBP	
0040BA3F	. C3	RETN	

# OlllyDbg

## Assembly Patching Tutorial

- Assembly Patching
  - Double-click on the short jump



The screenshot shows the OlllyDbg assembly window with a dialog box titled "Assemble at 0040BA11" open over it. The assembly window displays the following code:

```
0040BA0F . 85C0 TEST EAX,EAX
0040BA11 . 74 0F JE SHORT keygenme.0040BA22
0040BA13 . 68 34004200 PUSH OFFSET keygenme.??_C@_0BE@JMCA@add
0040BA18 . E8 83575555 CALL keygenme.??_C@_0BE@JMCA@add
0040BA1D . 83C4 04 INC ESP
0040BA20 . EB 0D MOV EBX,EBP
0040BA22 > 68 1C004200 PUSH OFFSET keygenme.??_C@_0BE@JMCA@add
0040BA27 . E8 74575555 CALL keygenme.??_C@_0BE@JMCA@add
0040BA2C . 83C4 04 INC ESP
0040BA2F > 5F POP EDI
0040BA30 . 5E POP EBX
0040BA31 . 5B POP EBX
0040BA32 . 83C4 04 INC ESP
0040BA35 . 3BEC CMP ECX,ECX
0040BA37 . E8 E4575555 CALL keygenme.??_C@_0BE@JMCA@add
0040BA3C . 8BE5 MOV EBX,ESP
0040BA3E . 5D POP EBX
0040BA3F . C3 RETN
```

The dialog box "Assemble at 0040BA11" contains the following text:

Assemble at 0040BA11

JE SHORT 0040BA22

Fill with NOP's

Buttons: Assemble, Cancel

# OlllyDbg

## Assembly Patching Tutorial

- Assembly Patching
  - Change the jump address

0040BA0F	. 85C0	TEST EAX,EAX	
0040BA11	· 74 0F	JE SHORT keygenme.0040BA22	
0040BA13	· 68 34004200	PUSH OFFSET keygenme.??_C@_0BE@JMCA@add	[format = "Valid license key!!!"
0040BA18	· E8 8357FFFF	CALL keygenme.printf	printf
0040BA1D	· 83C4 04	ADD ESP,4	
0040BA20	· EB 0D	JMP SHORT keygenme.0040BA2F	
0040BA22	> 68 1C004200	PUSH OFFSET keygenme.??_C@_0BF@CGOC@xor	[format = "Invalid license key..."
0040BA27	· E8 7457FFFF	CALL keygenme.printf	printf
0040BA2C	· 83C4		
0040BA2F	> 5F		
0040BA30	· 5E		
0040BA31	· 5B		
0040BA32	· 83C4		
0040BA35	· 3BEC		
0040BA37	· E8 E4		
0040BA3C	· 8BE5		
0040BA3E	· 5D		
0040BA3F	· C3		
0040BA40	· CC		
0040BA41	· CC		
0040BA42	· CC		

Assemble at 0040BA11

JE SHORT 0040BA13

Fill with NOP's

Assemble Cancel

# OlllyDbg

## Assembly Patching Tutorial

- Assembly Patching
  - Hit assemble
  - Check that the size of the code hasn't changed

0040BA0F	. 85C0	TEST EAX,EAX	
0040BA11	√74 00	JE SHORT keygenme.0040BA13	
0040BA13	. 68 34004200	PUSH OFFSET keygenme.??_C@_0BE@JMCA@add	[format = "Valid license key!!!"]
0040BA18	. E8 8357FFFF	CALL keygenme.printf	printf
0040BA1D	. 83C4 04	ADD ESP,4	
0040BA20	√EB 0D	JMP SHORT keygenme.0040BA2F	
0040BA22	> 68 1C004200	PUSH OFFSET keygenme.??_C@_0BF@CGOC@xor	[format = "Invalid license key..."]
0040BA27	. E8 7457FFFF	CALL keygenme.printf	printf
0040BA2C	. 83C4 04	ADD ESP,4	
0040BA2F	> 5F	POP ESI	
0040BA30	. 5E	POP EDI	
0040BA31	. 5B	POP EBX	
0040BA32	. 83C4 04	ADD ESP,4	
0040BA35	. 3BEC	CMPEB ECX,ESI	
0040BA37	. E8 E457FFFF	CALL keygenme.printf	printf
0040BA3C	. 8BE5	MOV ESI,EBP	
0040BA3E	. 5D	POP EDI	
0040BA3F	. C3	RETN	
0040BA40	. CC	INT3	
0040BA41	. CC	INT3	
0040BA42	. CC	INT3	

Assemble at 0040BA13

JE SHORT 0040BA13

Fill with NOP's

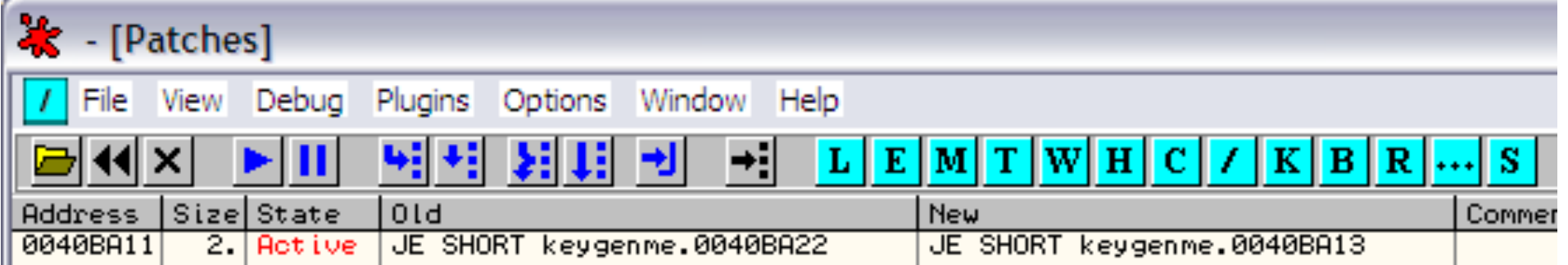
Assemble Cancel



# OlllyDbg

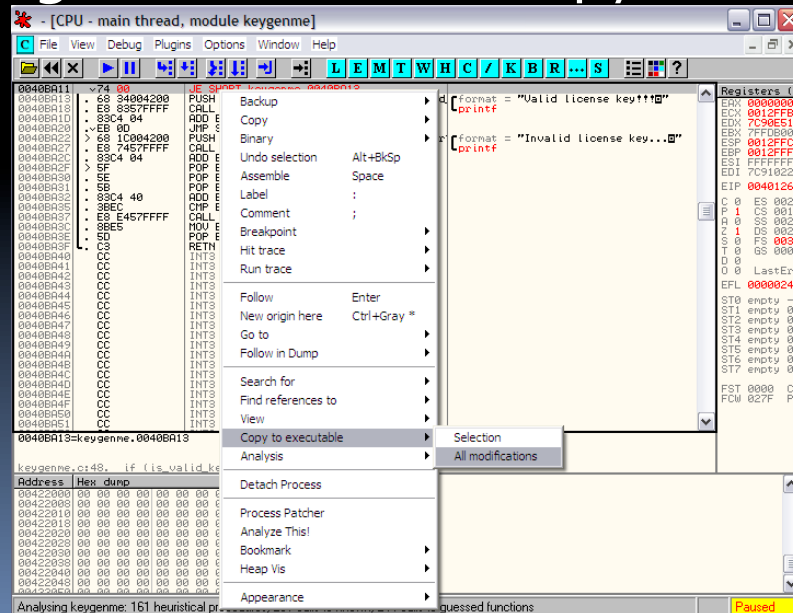
## Assembly Patching Tutorial

- Assembly Patching
  - View patches
    - Click on the “/” toolbar button or hit Ctrl+P
    - Right click on an entry and click “Follow in Disassembler” to return to the disassembler at the target address



# OllyDbg Assembly Patching Tutorial

- Assembly Patching
  - Right click on the patch
  - “Copy to executable” → “All Modifications”
  - OllyDbg 2.0: “Edit” → “Copy to executable”



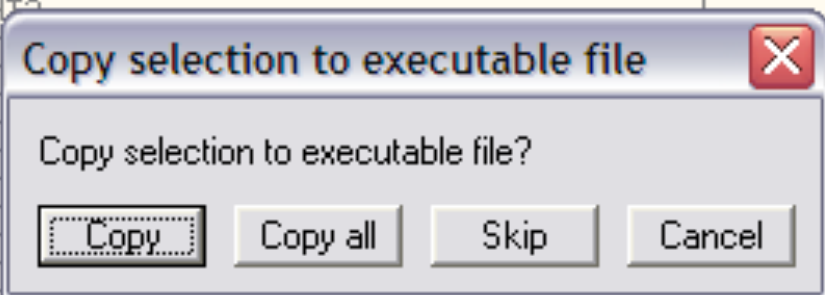
# OllyDbg

## Assembly Patching Tutorial

- Assembly Patching
  - Select "Copy all" (OllyDbg 1.\* only)

```
0040BA11  v74 00 JE SHORT keygenme.0040BA13
0040BA13  . 68 34004200 PUSH OFFSET keygenme.??_C@_0BE@JMCA@add
0040BA18  . E8 8357FFFF CALL keygenme.printf
0040BA1D  . 83C4 04 ADD ESP,4
0040BA20  .vEB 00 JMP SHORT keygenme.0040BA2F
0040BA22  > 68 1C004200 PUSH OFFSET keygenme.??_C@_0BF@CGOC@xor
0040BA27  . E8 7457FFFF CALL keygenme.printf
0040BA2C  . 83C4 04 ADD ESP,4
0040BA2F  > 5F POP EDI
0040BA30  . 5E POP ESI
0040BA31  . 5B POP EBX
0040BA32  . 83C4 40 ADD ESP,40
0040BA35  . 3BEC CMP EBP,ESP
0040BA37  . E8 E457FFFF CALL keygenme.__chkesp
0040BA3C  . 8BE5 MOV ESP,EBP
0040BA3E  . 5D POP EBP
0040BA3F  . C3 RETN
0040BA40  CC INT3
0040BA41  CC INTP
0040BA42  CC INTP
0040BA43  CC INTP
0040BA44  CC INTP
0040BA45  CC INTP
0040BA46  CC INTP
0040BA47  CC INTP
0040BA48  CC INTP
0040BA49  CC INTP
0040BA4A  CC INTP
```

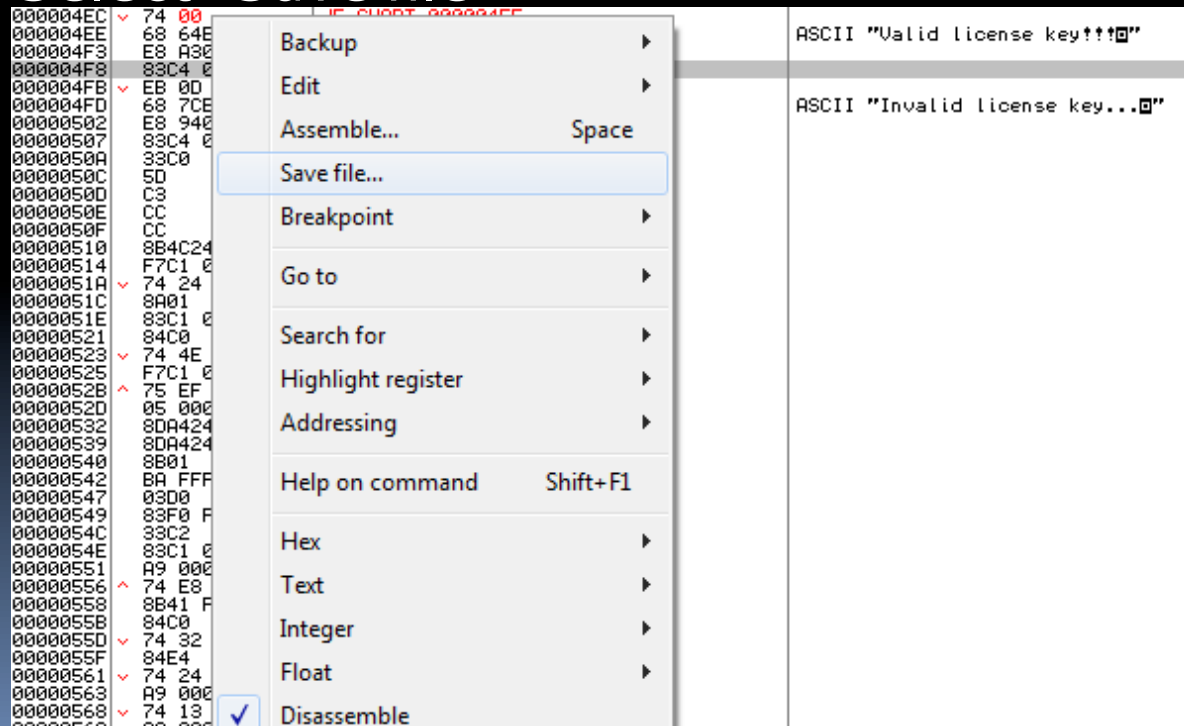
format = "Valid license key!!!\n" printf  
format = "Invalid license key...\n" printf



# OlllyDbg

## Assembly Patching Tutorial

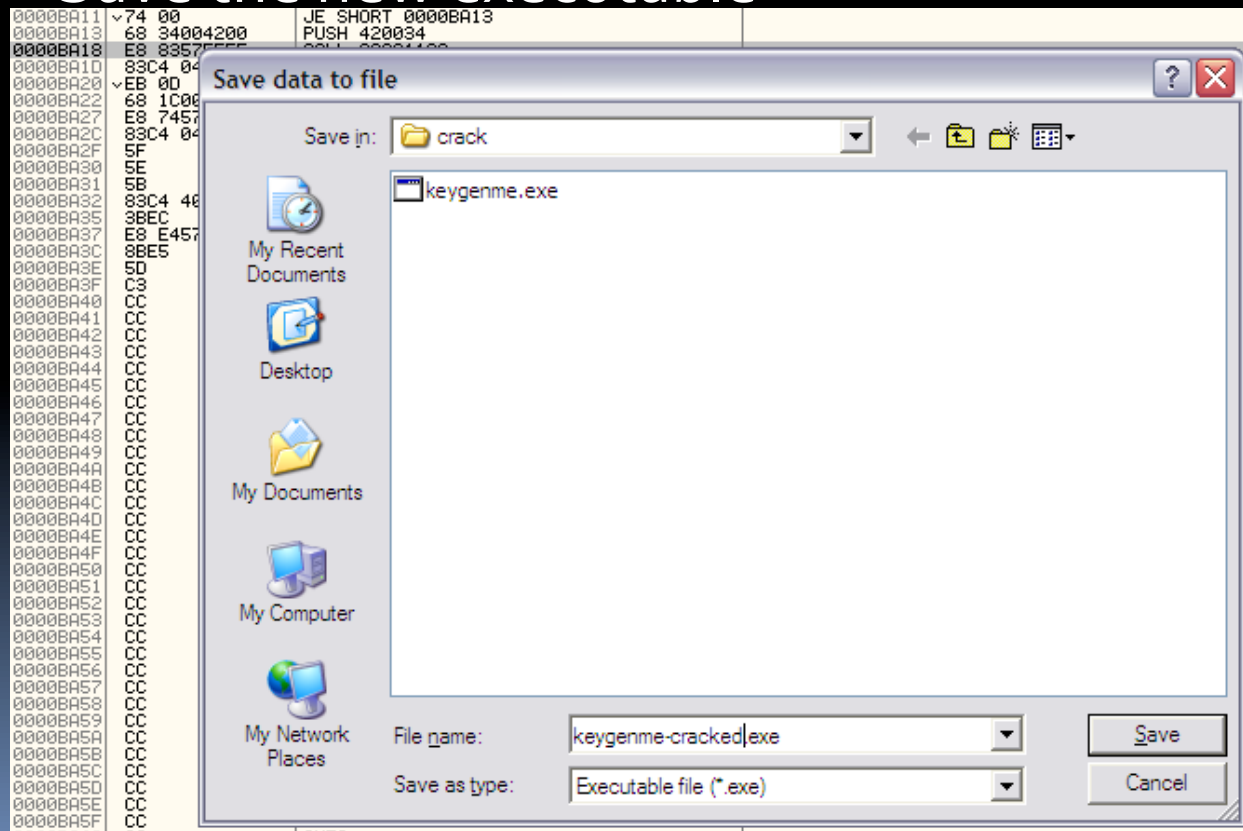
- Assembly Patching
  - Right click in the newly generated executable
  - Select "Save file"



# OlllyDbg

## Assembly Patching Tutorial

- Assembly Patching
  - Save the new executable





# IDA

- Disassembler, Decompiler\*, Debugger
  - Commercial debugger
    - With freeware and demo versions
  - Now a Hex-Rays product
    - Formerly Datarescue
  - \*Decompilers sold seperately (and is expensive)

# IDA

The screenshot displays the IDA Pro interface for the file `C:\Documents and Settings\Jojo\Desktop\Advanced Reverse Engineering\sample_code\crack\keygenme.exe`. The main window, titled "IDA View-A", shows a control flow graph (CFG) for a disassembled assembly routine. The graph consists of several basic blocks connected by control flow edges. The top block contains the instructions `cmp eax, 22h` and `jnz short loc_403666`. A red arrow indicates the flow to the left block, which contains `mov ecx, [ebp+var_4]`, `add ecx, 1`, `mov [ebp+var_4], ecx`, and `jmp short loc_40366D`. A green arrow indicates the flow to the right block, labeled `loc_403666:`, containing `mov [ebp+var_8], 0`. From the left block, a blue arrow points to the bottom-left block, labeled `loc_40366D:`, containing `jmp short loc_403676`. From the right block, a blue arrow points to the bottom-right block, labeled `loc_40366F:`, containing `mov [ebp+var_8], 0`. Both bottom blocks have blue arrows pointing to a common exit point at the bottom of the graph. A "Graph overview" window is visible on the left side of the main window. The status bar at the bottom indicates "AU: idle", "Down", and "Disk: 2GB".

IDA - C:\Documents and Settings\Jojo\Desktop\Advanced Reverse Engineering\sample\_code\crack\keygenme.exe

File Edit Jump Search View Debugger Options Windows Help

IDA View-A Hex View-A Exports Imports Names Functions Strings Structures Enums

Graph overview

```
cmp    eax, 22h
jnz    short loc_403666
```

```
loc_403666:
mov    [ebp+var_8], 0
```

```
loc_40366D:
jmp    short loc_403676
```

```
loc_40366F:
mov    [ebp+var_8], 0
```

Propagating type information...  
Function argument information is propagated  
The initial autoanalysis has been finished.

AU: idle Down Disk: 2GB



# IDA

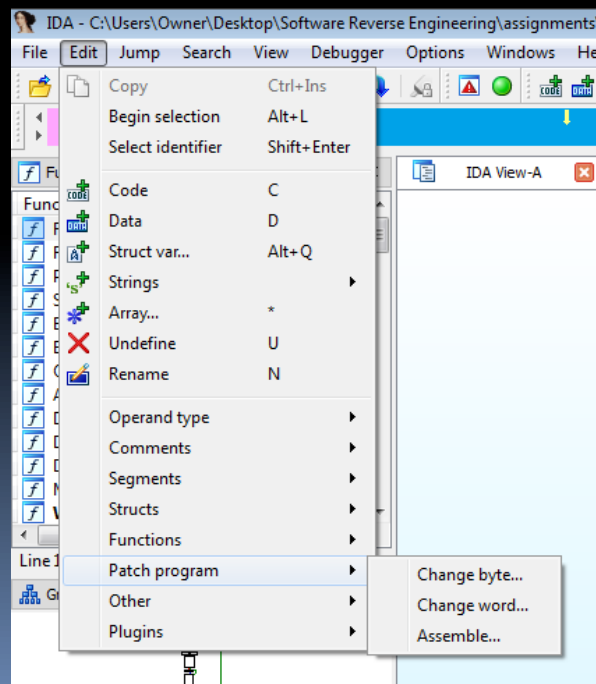
## ■ Shortcuts

- Run (F9), step into (F7), step over (F8)
- Set/clear breakpoint (F2)
- Apply name to an address (N)
- Comment (:), repeatable comment (;)
- Toggle graph view/assembly view (space)
- Jump to name/address (G)
- Follow reference (enter)
- Display/jump to cross-references (X)
- Return to previous location (esc)



# IDA Patching

- Patching
  - Edit "cfg/idagui.cfg"
  - Change "DISPLAY\_PATCH\_SUBMENU" to "YES"



# Hex-Rays Decompilers

```
; ===== SUBROUTINE =====
; Attributes: bp-based frame
; sge11(__int64, __int64)
public @sge11$qjj
@sge11$qjj    proc near

arg_0        = dword ptr  8
arg_4        = dword ptr  0Ch
arg_8        = dword ptr  10h
arg_C        = dword ptr  14h

        push    ebp
        mov     ebp, esp
        mov     eax, [ebp+arg_0]
        mov     edx, [ebp+arg_4]
        cmp     edx, [ebp+arg_C]
        jnz    short loc_10226
        cmp     eax, [ebp+arg_8]
        setnb  al
        jmp    short loc_10229

; -----
loc_10226:    setnl   al                ; CODE XREF: sge11(__int64,__int64)+Cj

loc_10229:    and     eax, 1                ; CODE XREF: sge11(__int64,__int64)+14j
        pop     ebp
        retn
@sge11$qjj    endp
```

```
bool __cdecl sge11(__int64 a1, __int64 a2)
{
    return a1 >= a2;
}
```

# WinDbg

- Disassembler, Debugger
  - User/kernel-mode debugger

The screenshot shows the WinDbg interface with the following components:

- Command Window:** Shows the command `Kernel 'com:port=com1,baud=115200' - WinDbg:6.0.0017.0`.
- Disassembly Window:** Displays assembly code for the function `Ntfs!NtfsExceptionFilter`. The instruction `fd8f2337 55 push ebx` is highlighted in red. Other instructions include `inc edx`, `or al,0x0`, `mov ebp,esp`, `push ecx`, `push ecx`, `cmp byte ptr [Ntfs!NtfsTestFilter (fd90aee2)],`, `push ebx`, `mov ebx,[ebp+0xc]`, `mov eax,[ebx]`, `push esi`, `push edi`, `mov edi,[eax]`, `jne Ntfs!NtfsExceptionFilter+0x18 (fd8fa8e9)`, and `cmp edi,0xc0000006`.
- Registers Window:** Shows the current state of registers. The `ebx` register contains the value `fd676a64`. Other registers include `gs` (0), `fs` (30), `es` (23), `ds` (23), `edi` (fd909998), `esi` (0), `edx` (80512bb7), `ecx` (c0000022), `eax` (fd6760d0), `ebp` (fd676a74), `eip` (fd8f2337), `cs` (8), `efl` (282), `esp` (fd6760a8), `ss` (10), `dr0` (0), `dr1` (0), `dr2` (0), `dr3` (0), `dr6` (ffff0ff0), `dr7` (400), `di` (9998), `si` (0), and `bx` (6a64).
- Calls Window:** Shows a list of function calls with columns for `ChildEBP`, `RetAddr`, `Args`, and `Child`. The first entry is `fd6760a4 fd9217a5 80dacda8 fd6760d0 80510f25 Ntfs!NtfsExceptionFilter`.



# Questions/Comments?

